

Figure 1

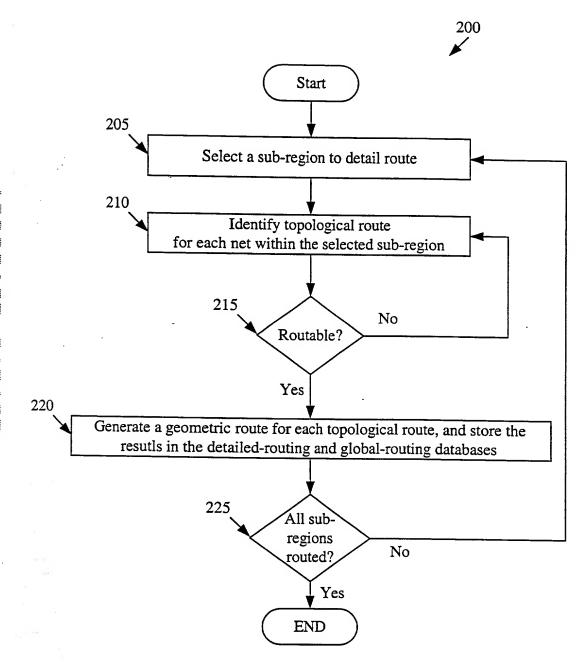
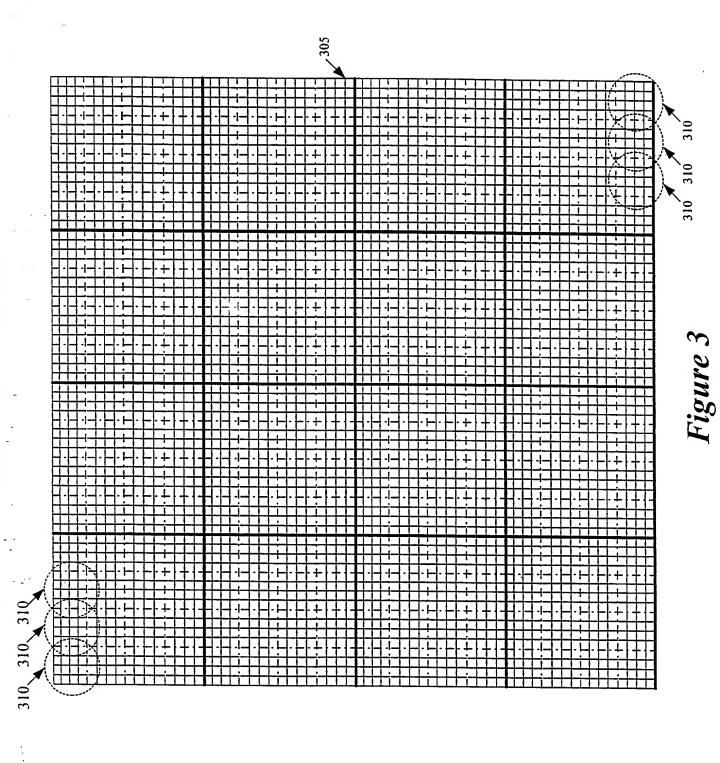


Figure 2



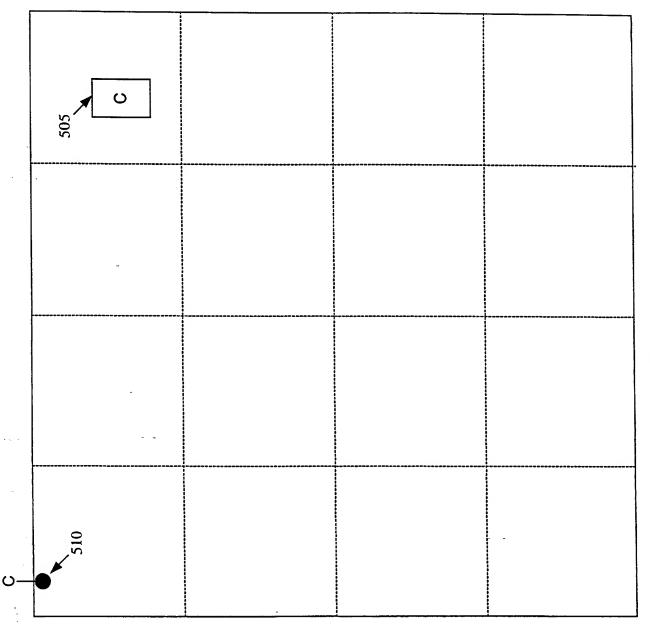


Figure 5

-List of Geometries

- -- Each Geometry including a sequence of points & layer assignment
 - -Bounding box of the region
 - -Array of layer properties
- --Minimum wire size
- ---Minimum spacing
- ---Via sizes
- --Cost/Unit
- -Netlist specifying a number of nets
- --Each net specifying a set of pins

-- Each pin specifying a set of ports

-- Each port specifying a set of geometries

Figure 6

- -List of Geometries
- -- Each Geometry including a sequence of points & layer assignment
 - --List of connection nodes inside each pin geometry
 - -Bounding box of the region
- -Array of layer properties
- --Minimum wire size
- --Minimum spacing
 - --Via sizes
- --Cost/Unit
- -Netlist specifying a number of nets
- -- Each net specifying a set of pins
- --Each pin specifying a set of ports
 --Each port specifying a set of geometries
- -For each layer, a graph specifying
 - --Nodes
- --Edges
 - --Faces

Figure 7

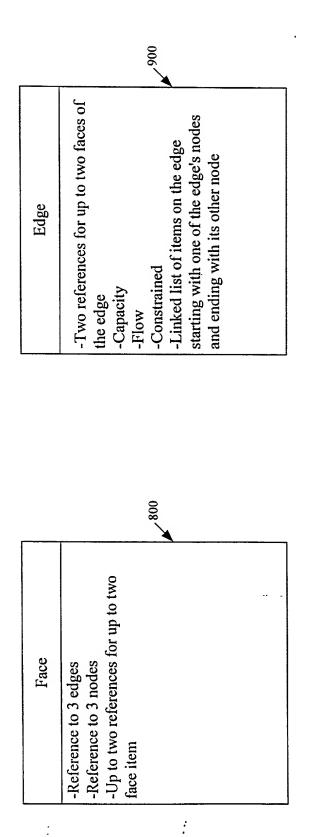


Figure 8

Figure 9

10

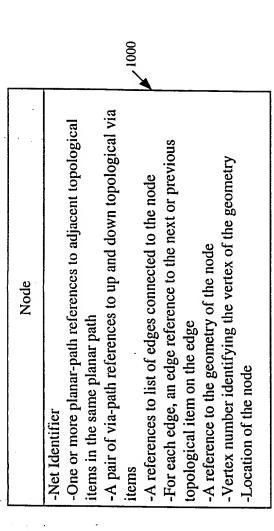
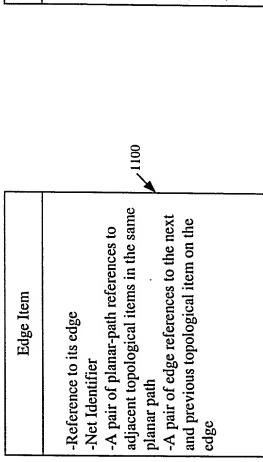


Figure 10



Face Item

-Reference to its face

-Net Identifier

-Up to 3 planar-path references for adjacent topological items in the same planar path

-A pair of via-path references for up and down topological via items

-Bounding polygon that defines legal face item locations

-Constraining Points and Distances

Figure 12

Figure 11

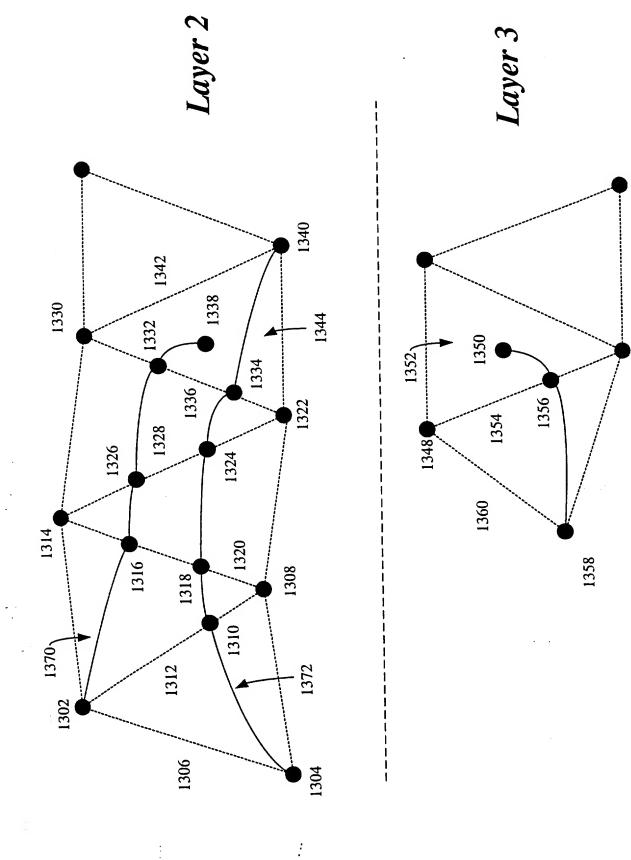
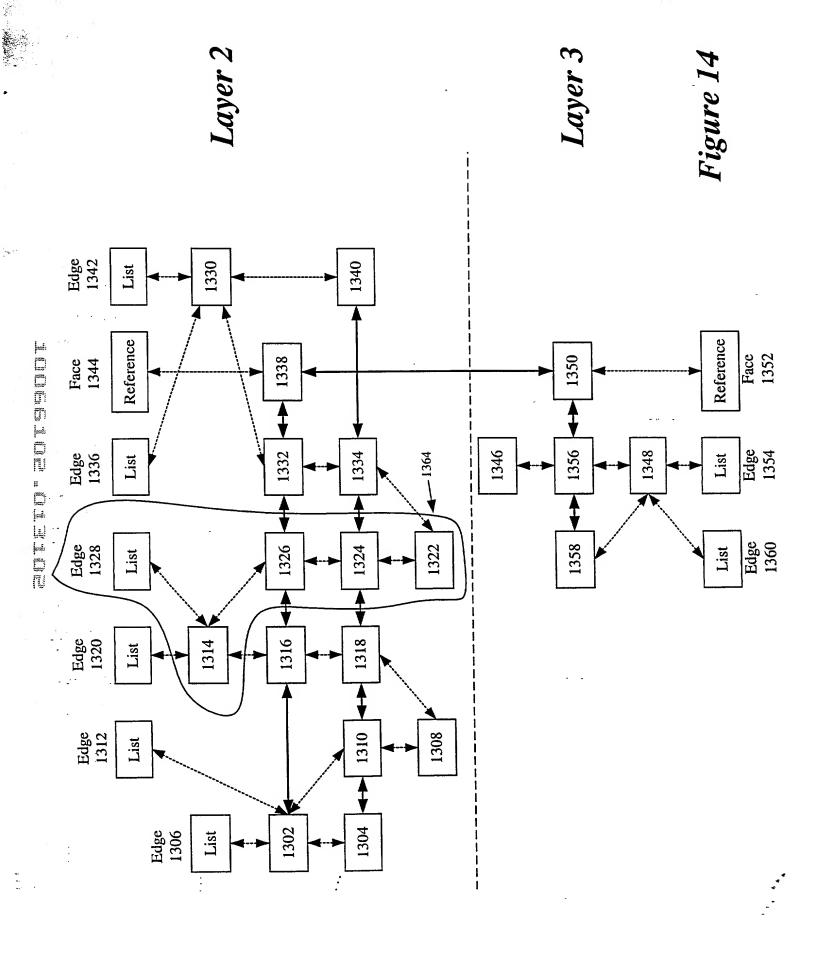
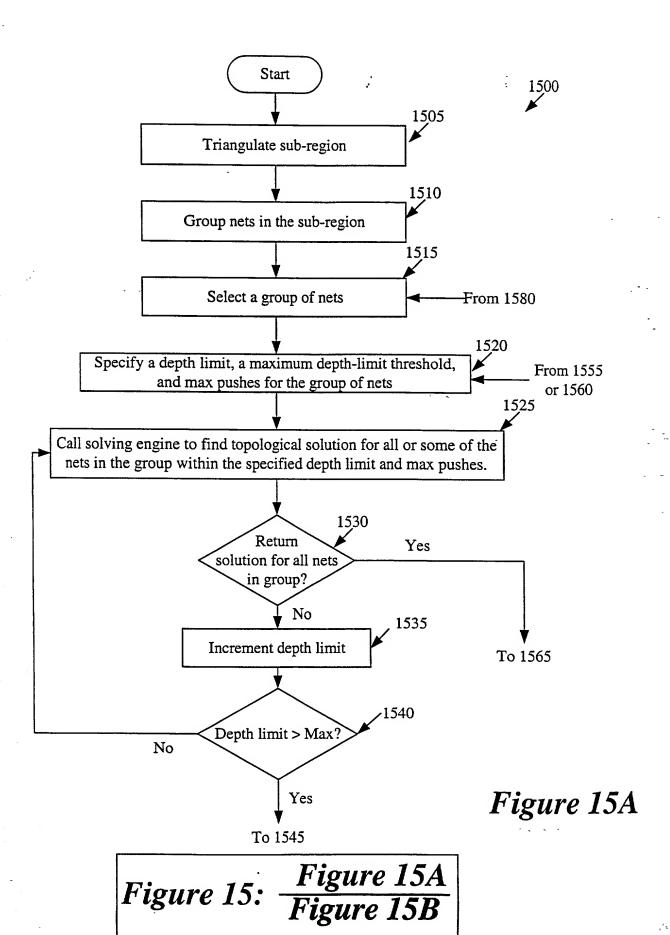
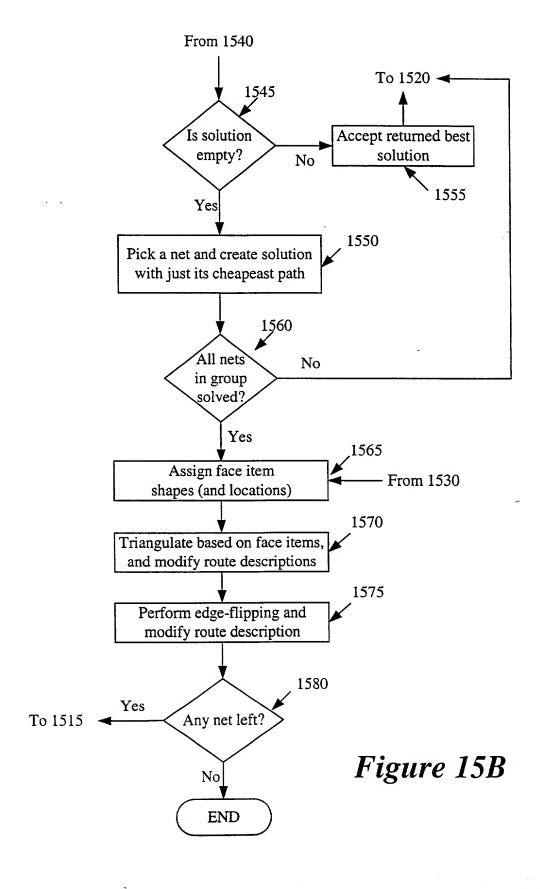
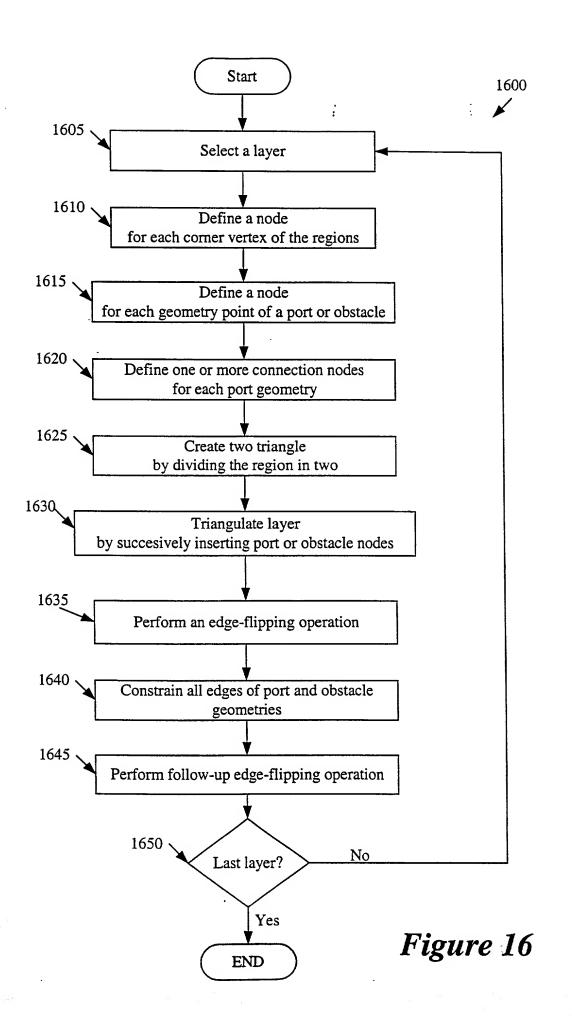


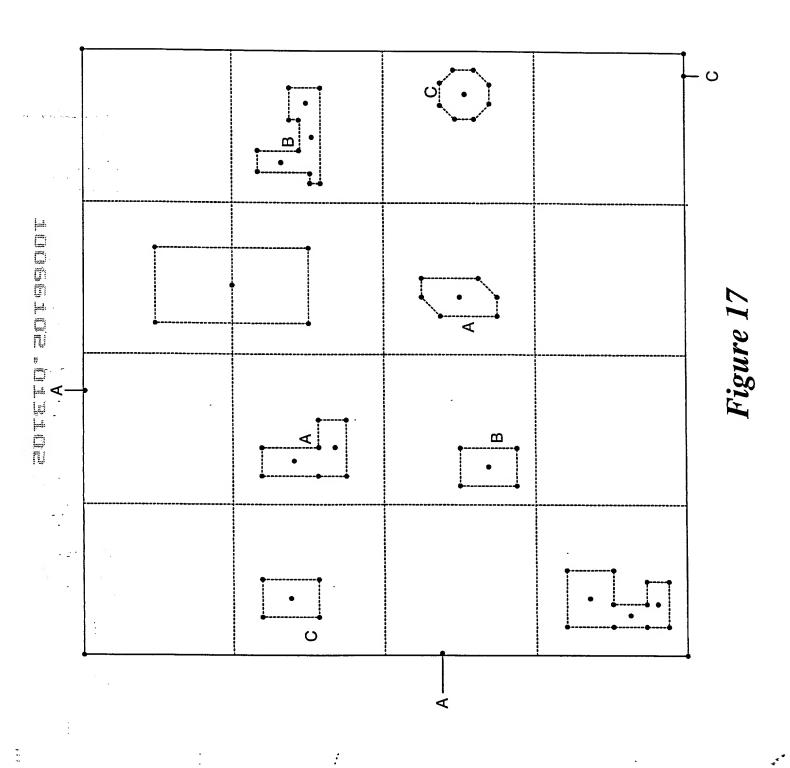
Figure 13











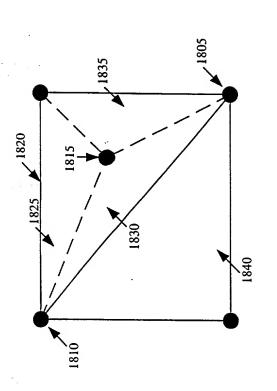


Figure 18

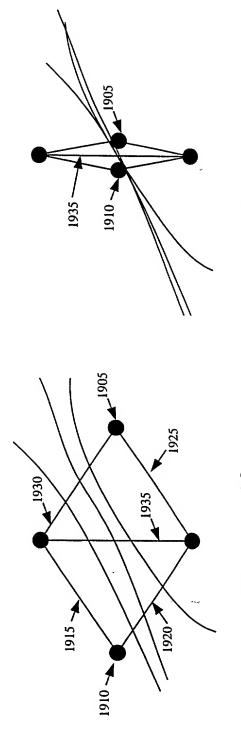


Figure 19

Figure 20

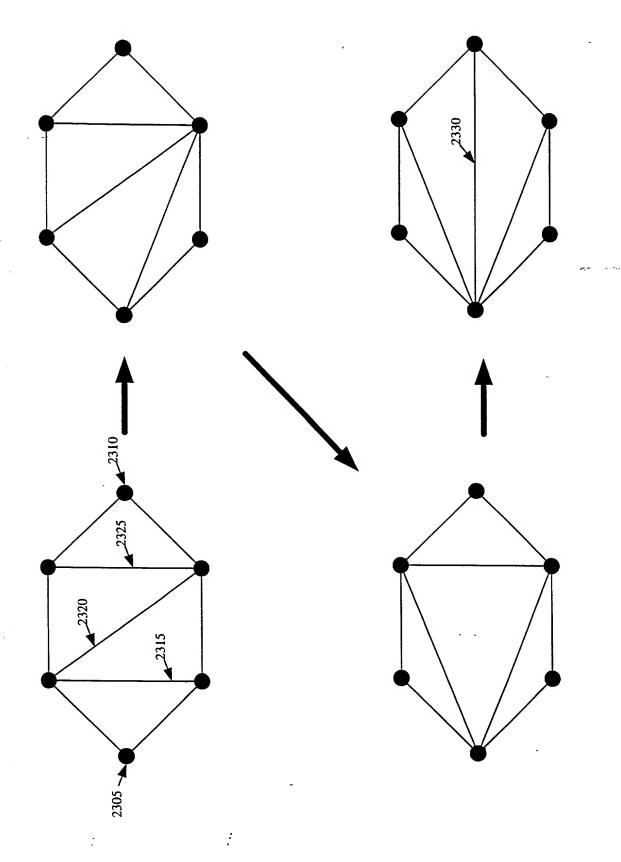
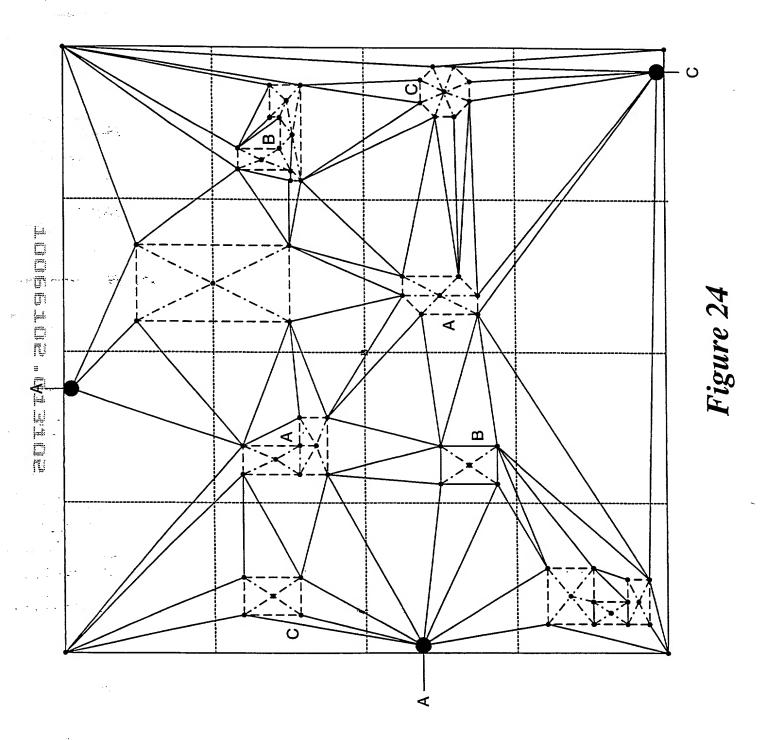


Figure 23



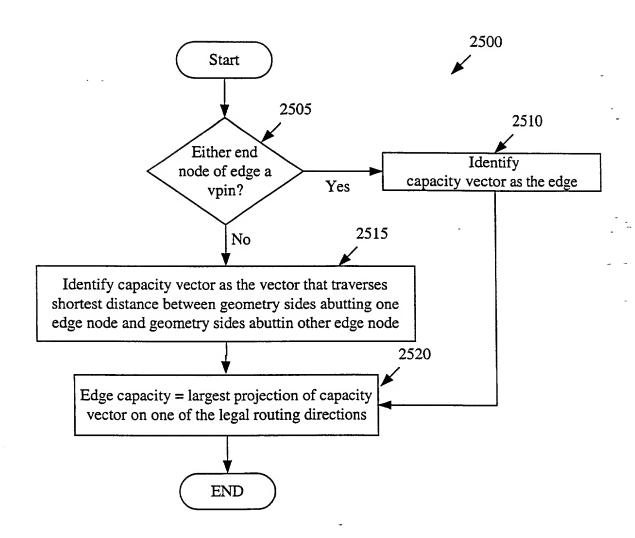


Figure 25

Figure 26

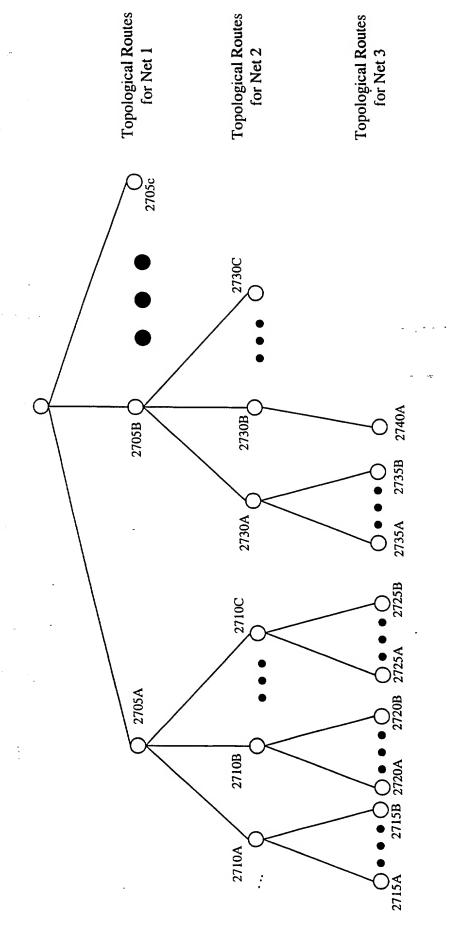


Figure 27

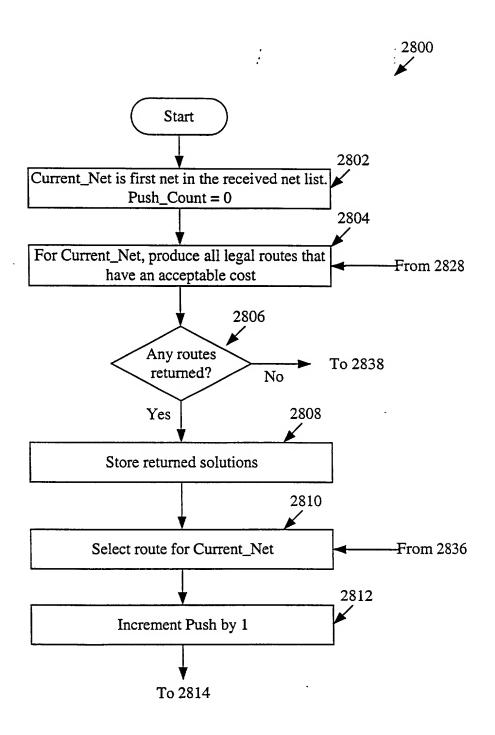
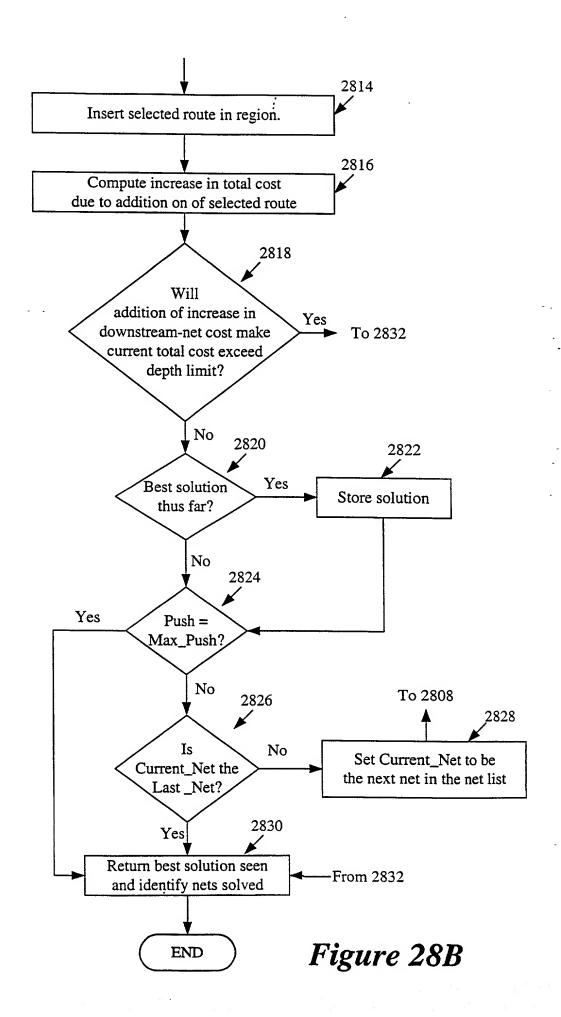


Figure 28A

Figure 28: Figure 28A Figure 28B Figure 28C



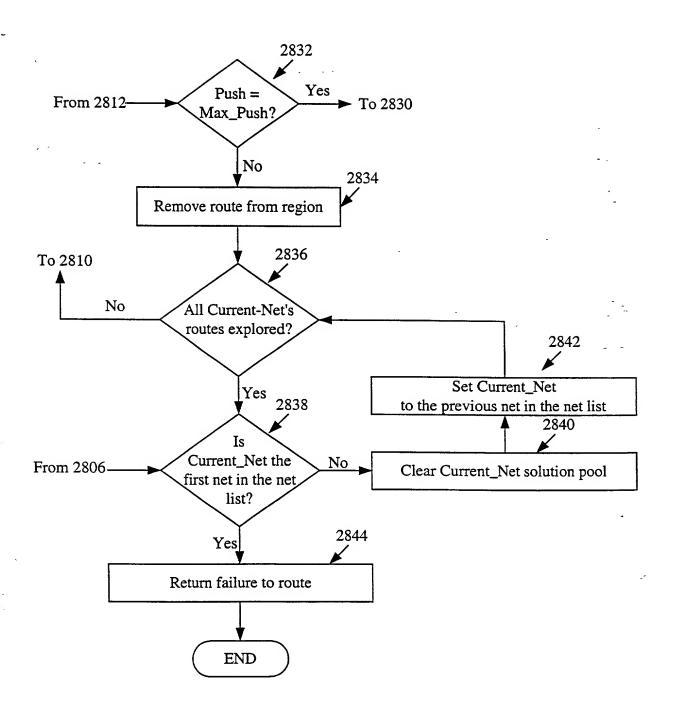


Figure 28C

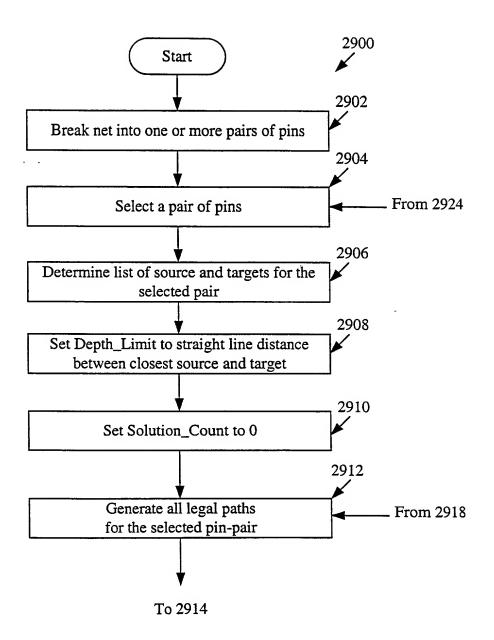
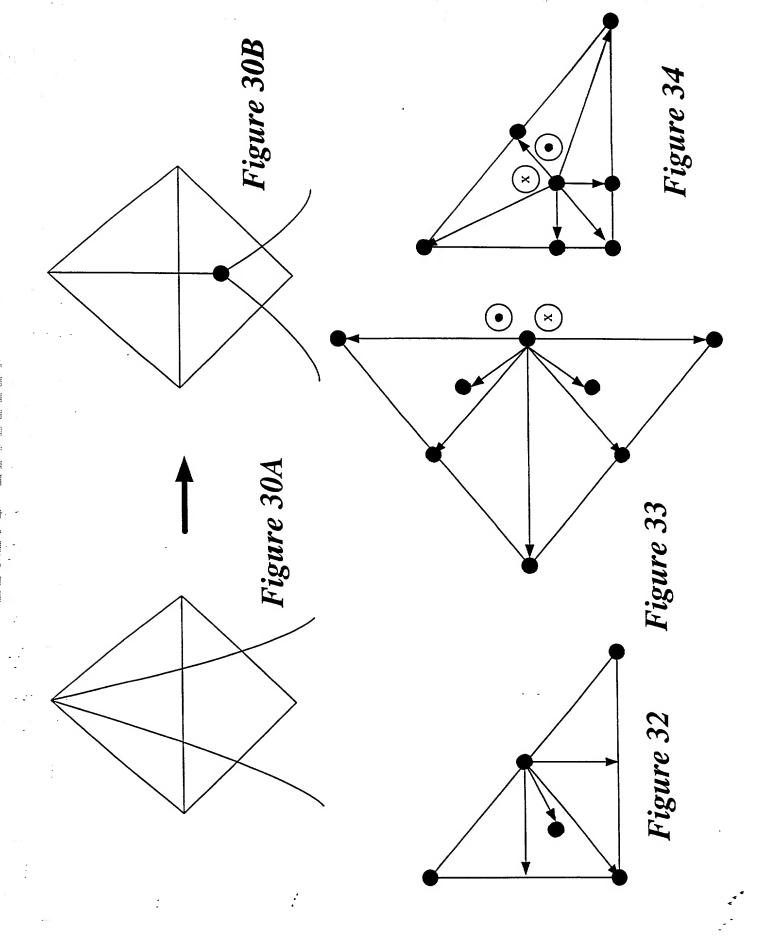


Figure 29A

Figure 29: Figure 29A Figure 29B



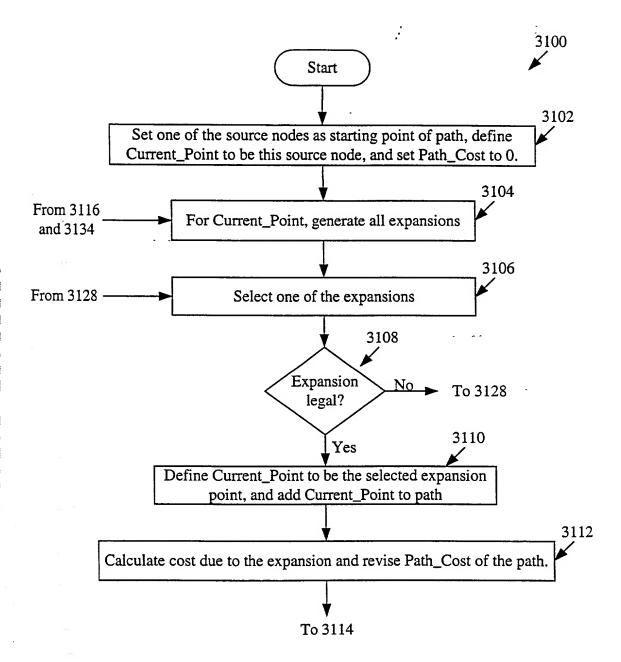
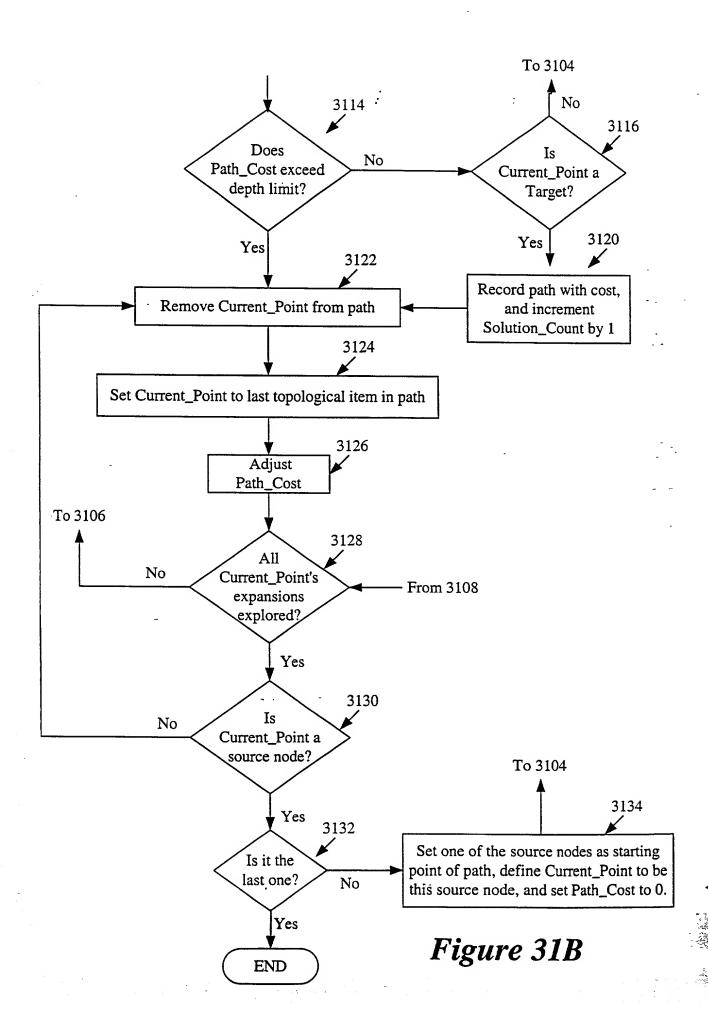
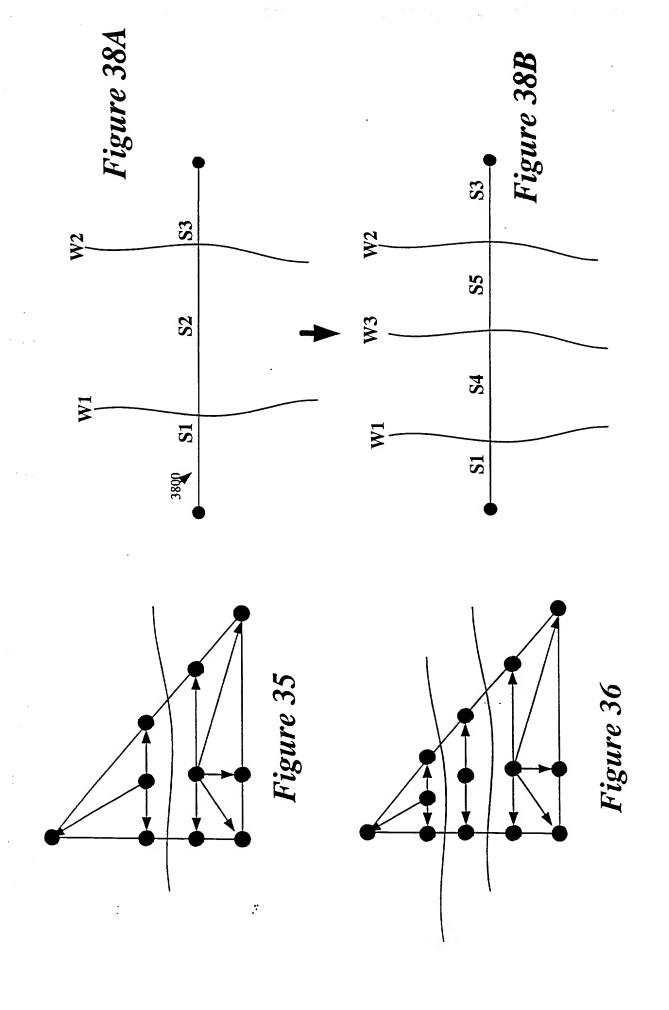


Figure 31A

Figure 31: Figure 31A Figure 31B





	To:	Node	Face Item	Edge Item
From: Node	• •	Planarity Vias	• Vias	PlanarityViasEdgeCapacity
Face Item	•	Vias	• Vias	ViasEdgeCapacity
Edge Item	• •	Planarity Vias	• Vias	PlanarityViasEdgeCapacity

Figure 37

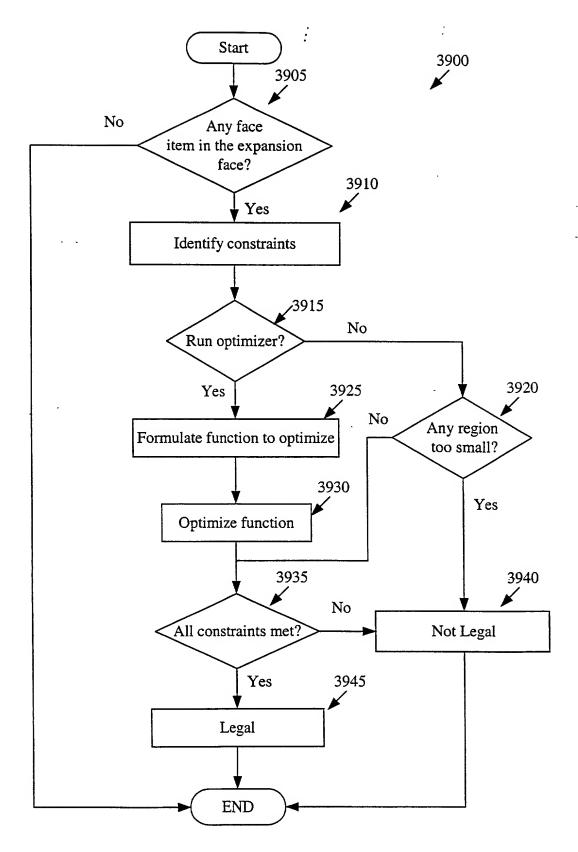


Figure 39A

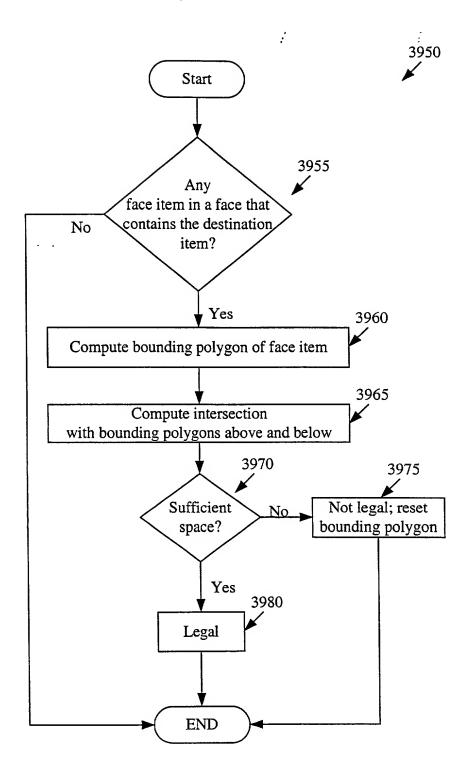
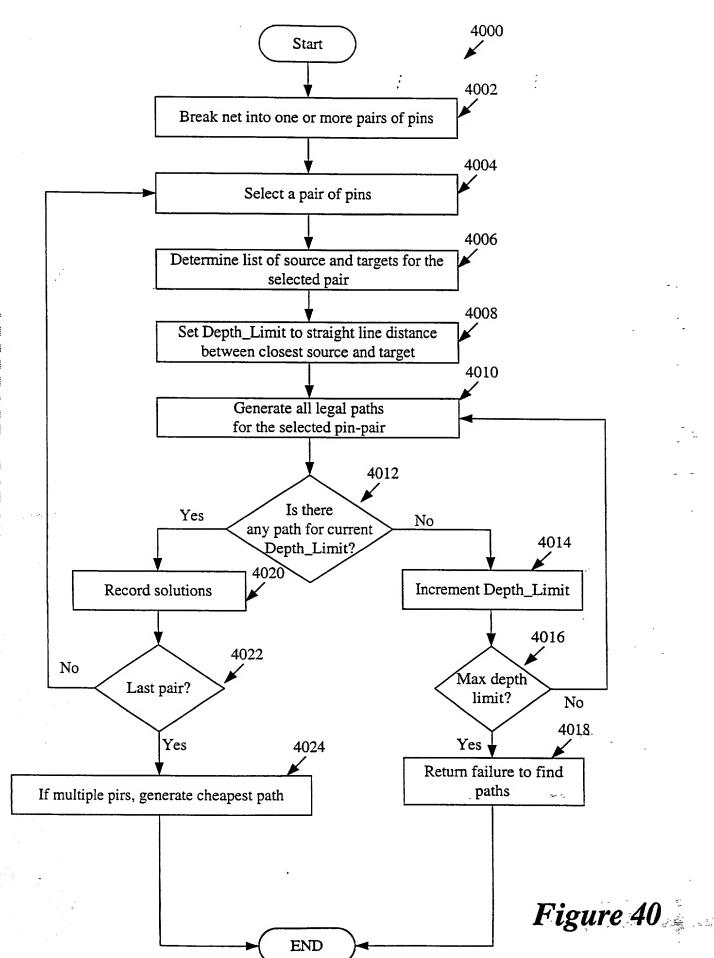


Figure 39B



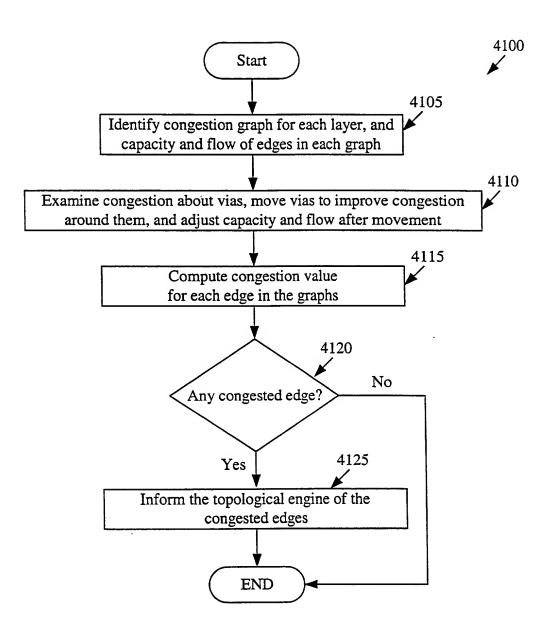


Figure 41

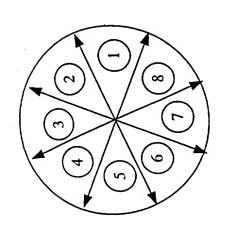


Figure 42

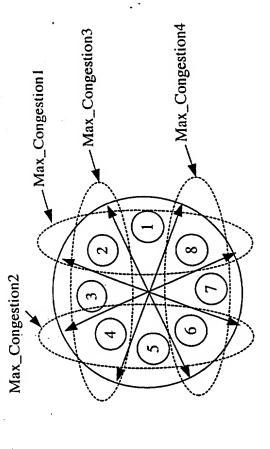


Figure 44

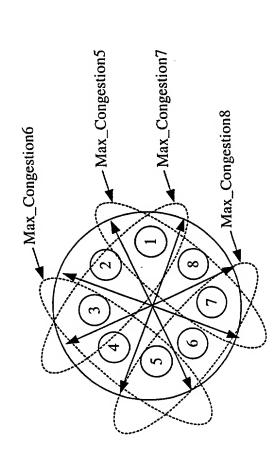


Figure 45

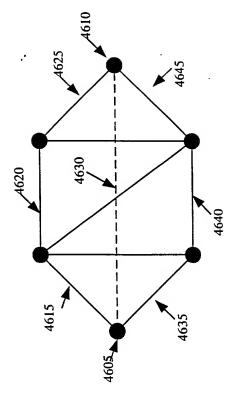
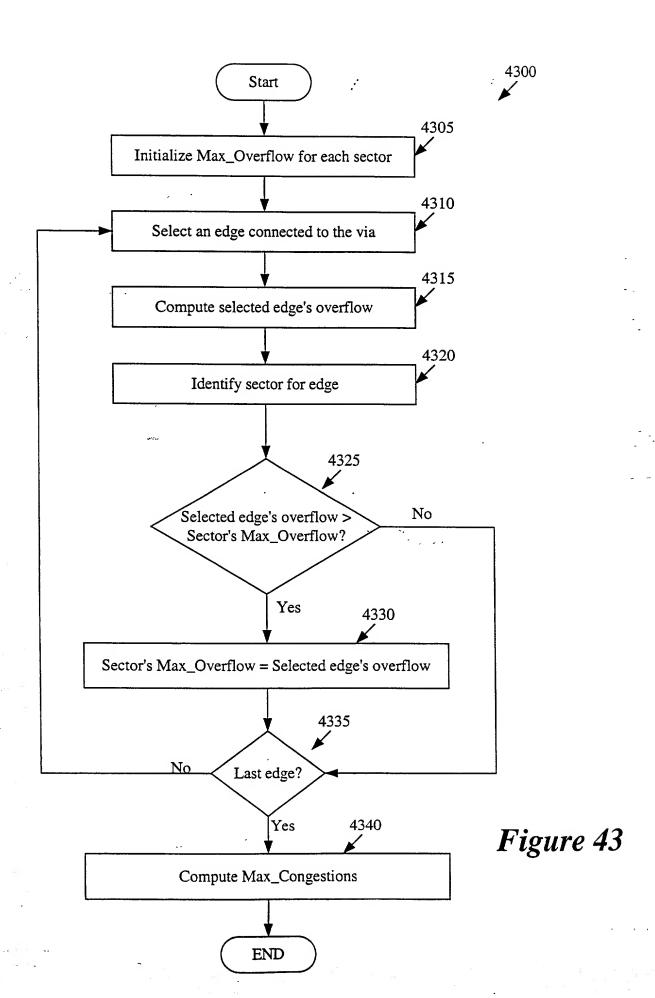


Figure 46



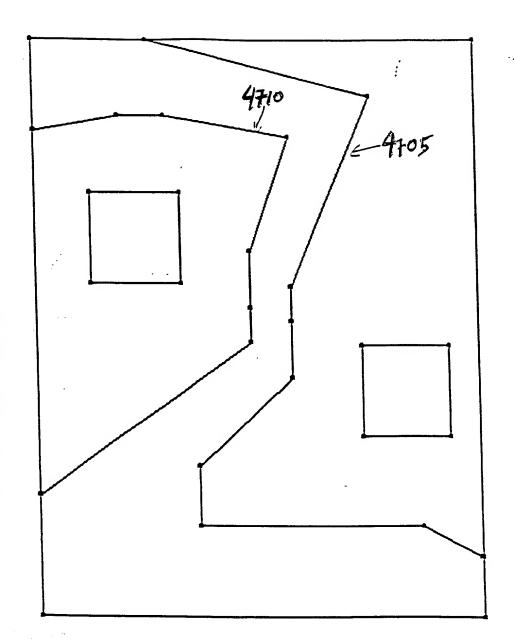
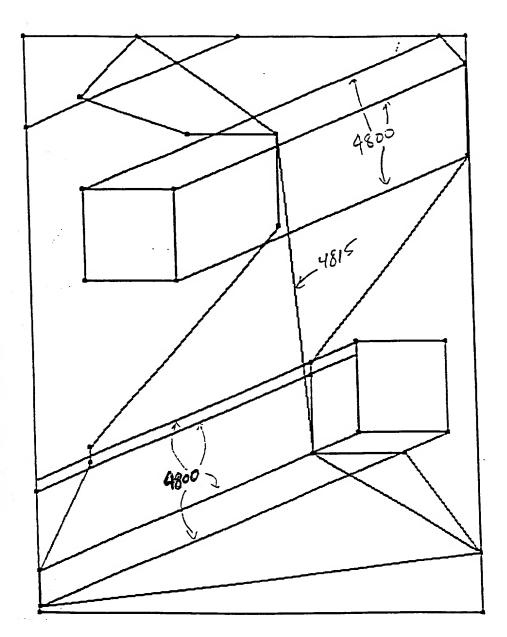


FIGURE 47



F164128 48A

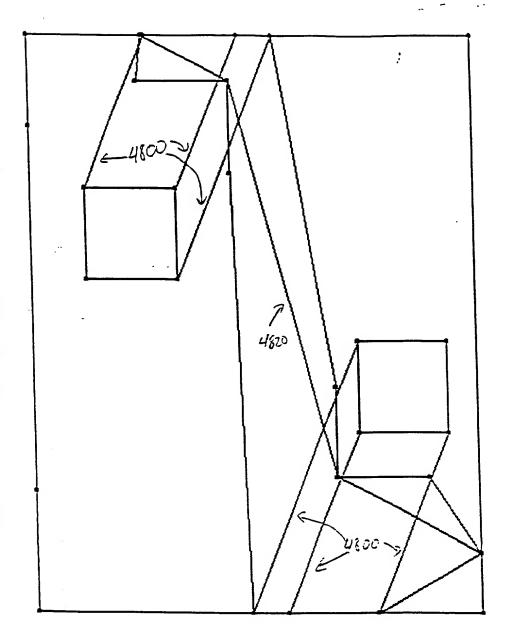


FIGURE 48B

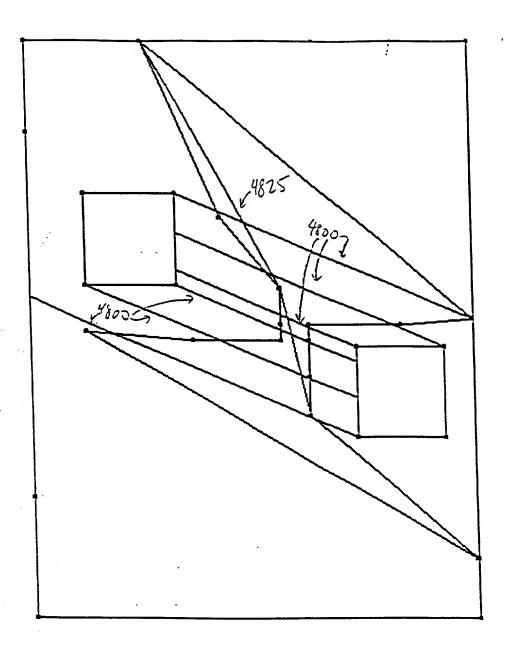


FIGURE 48C

FIGURE 48D

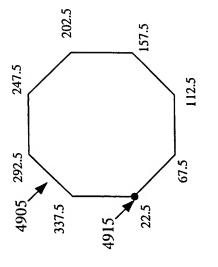


Figure 49A

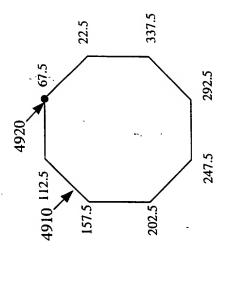


Figure 49B

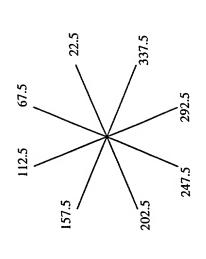


Figure 49C

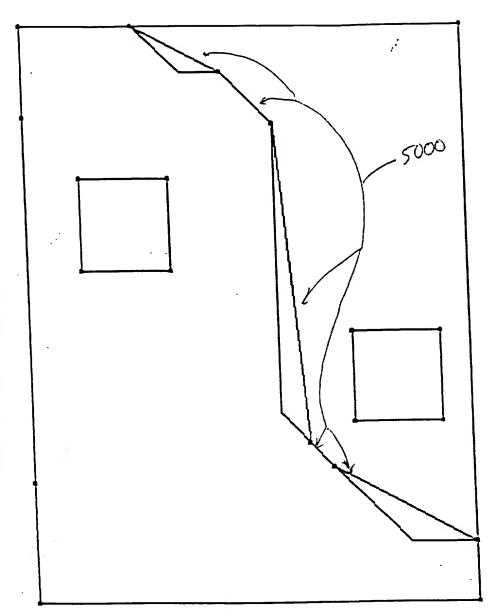


FIGURE 50

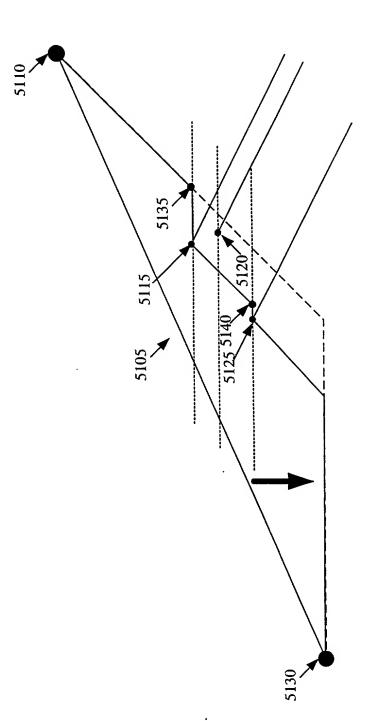


Figure 51

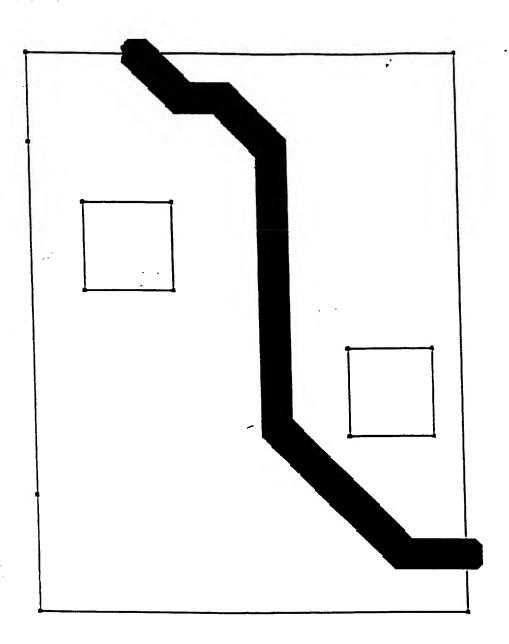


FIGURE 52

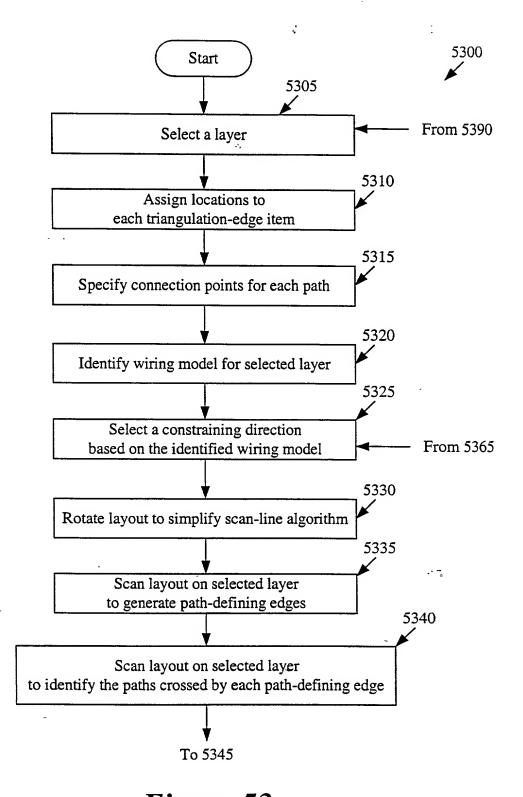
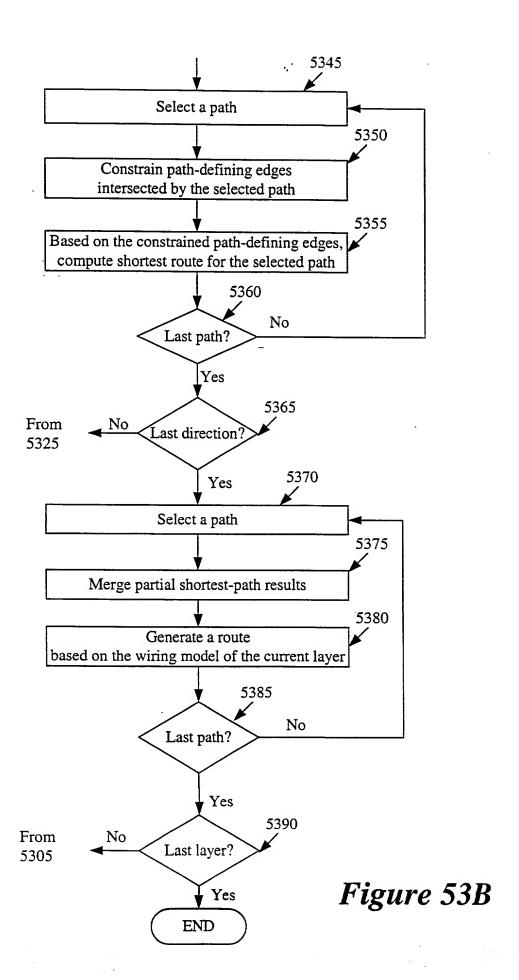
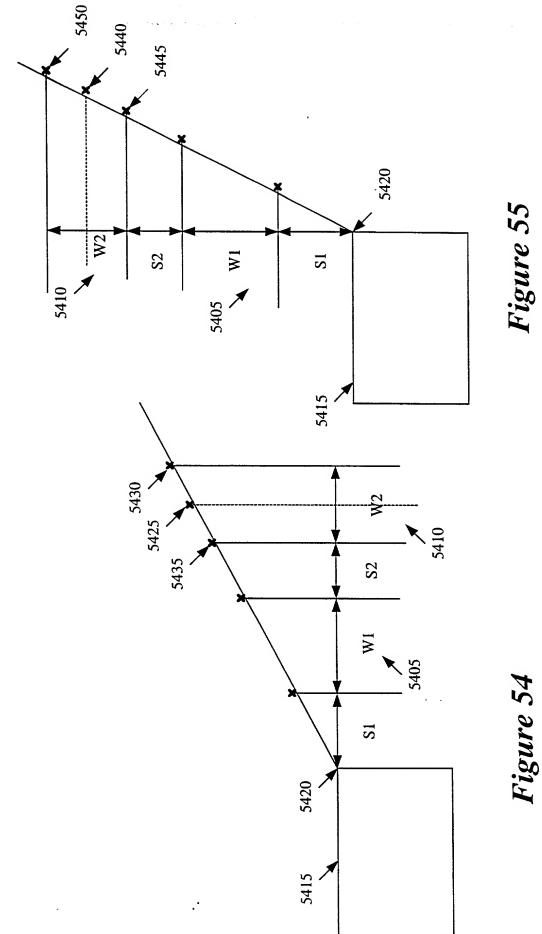
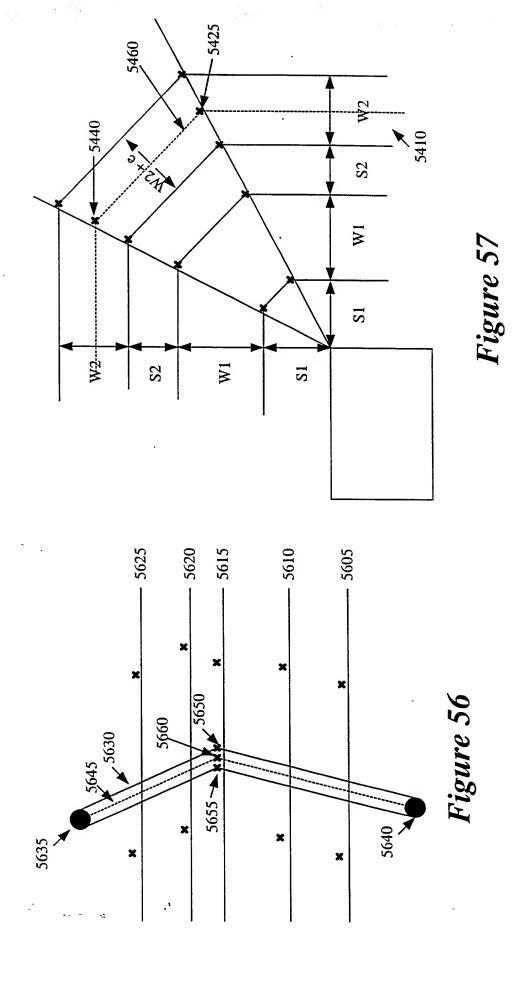


Figure 53
Figure 53A
Figure 53B







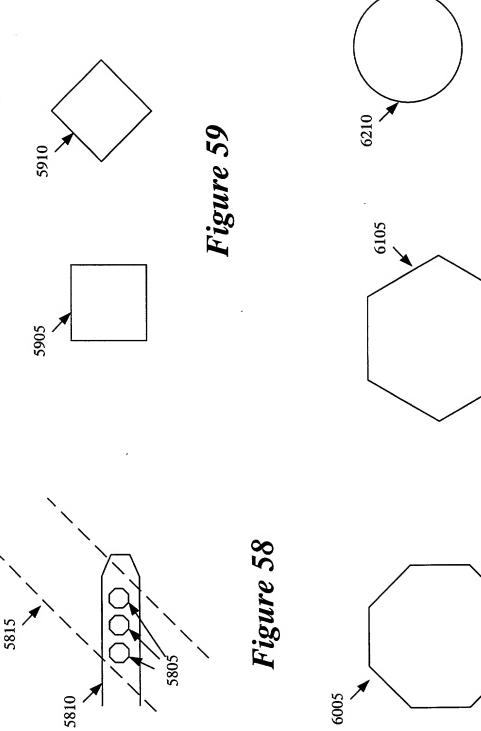


Figure 61

Figure 62

Figure 60

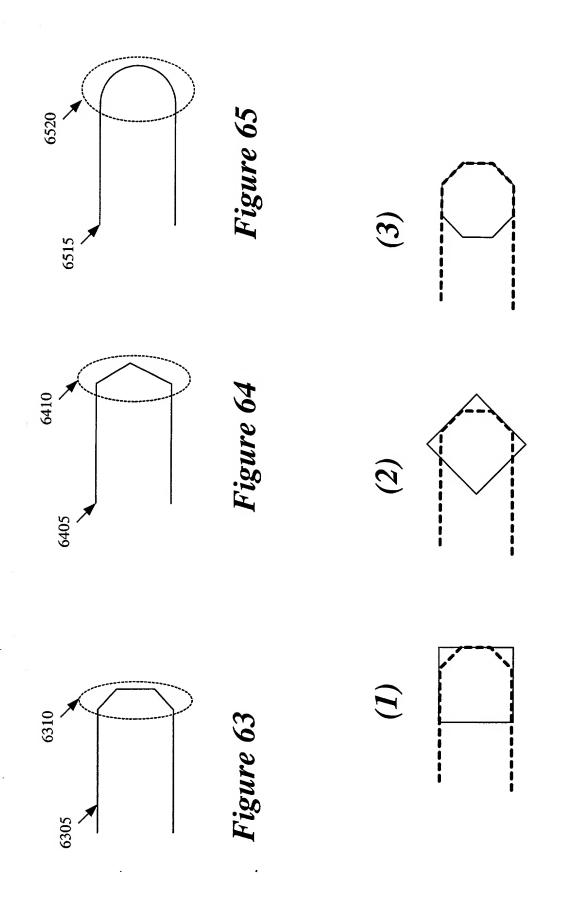
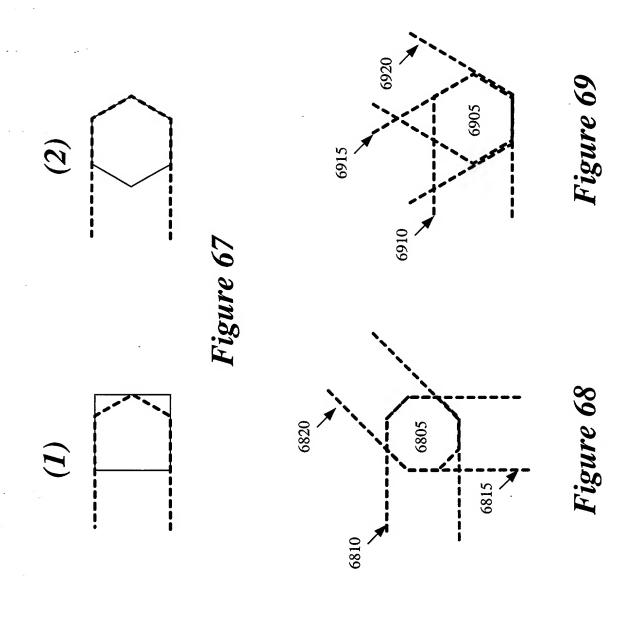
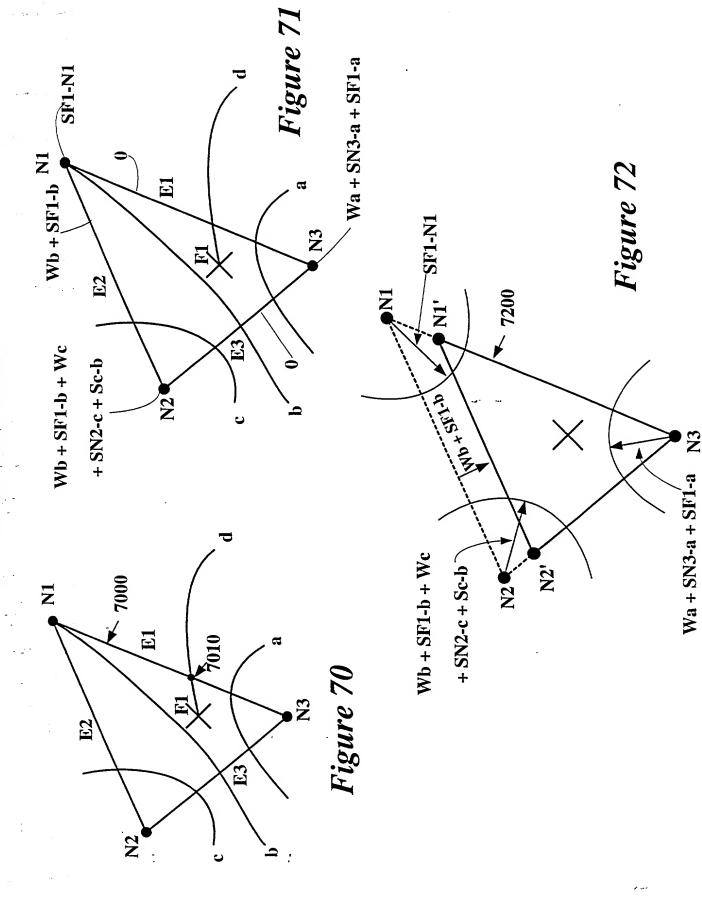
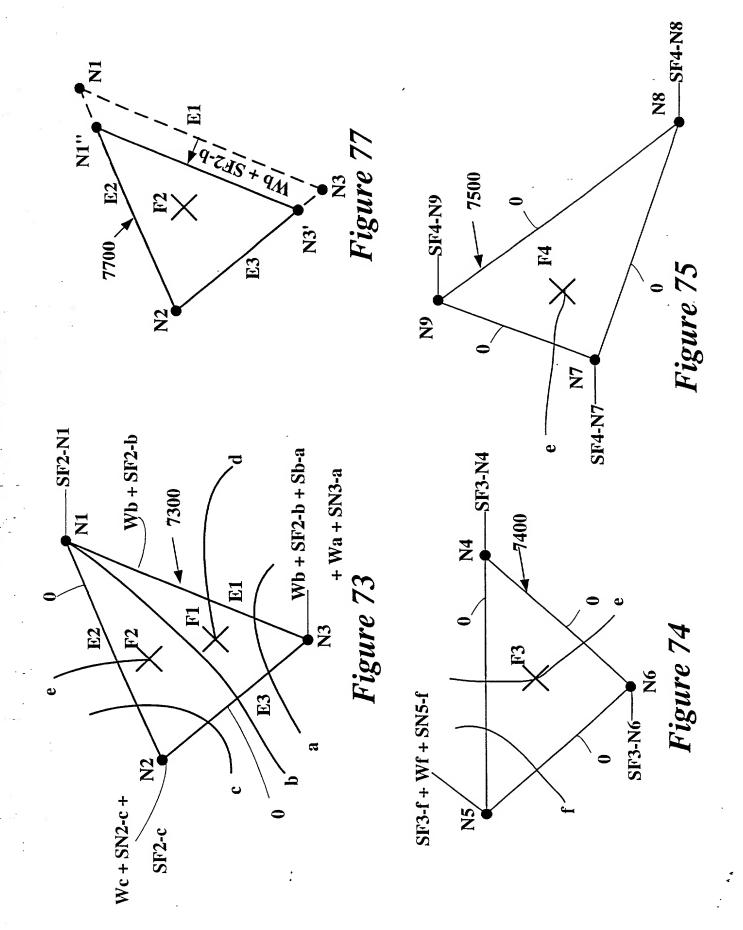
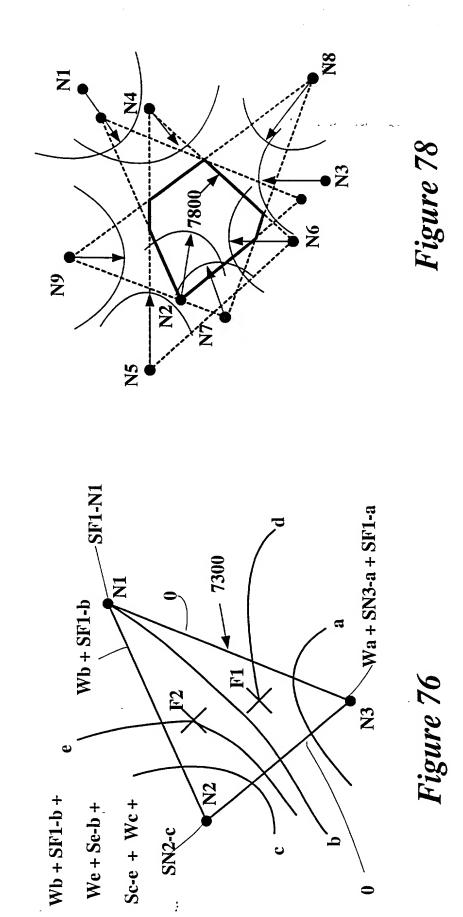


Figure 66









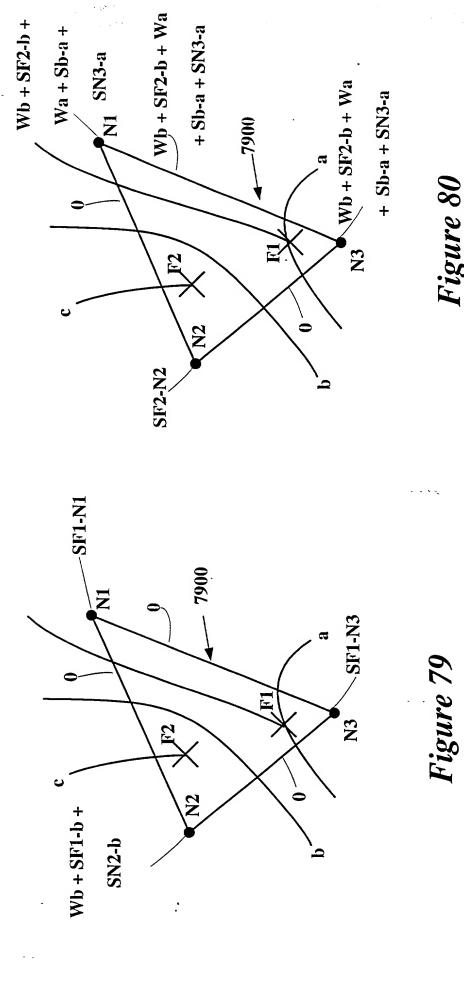
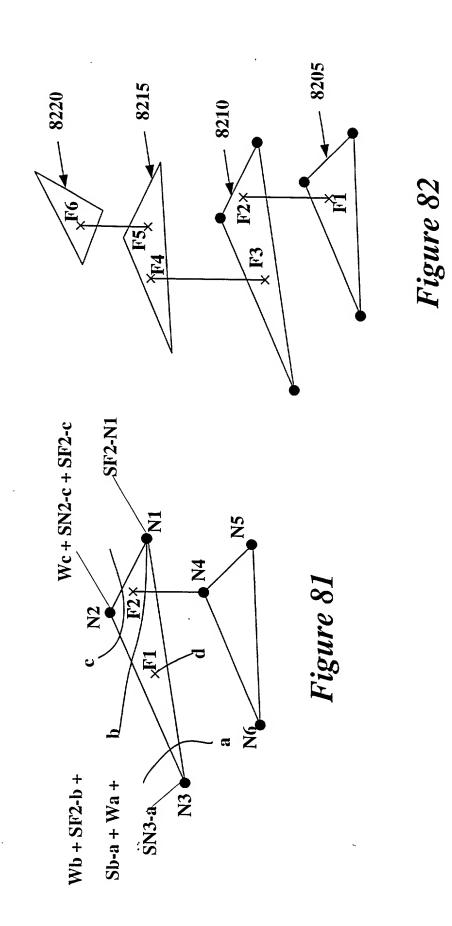


Figure 80



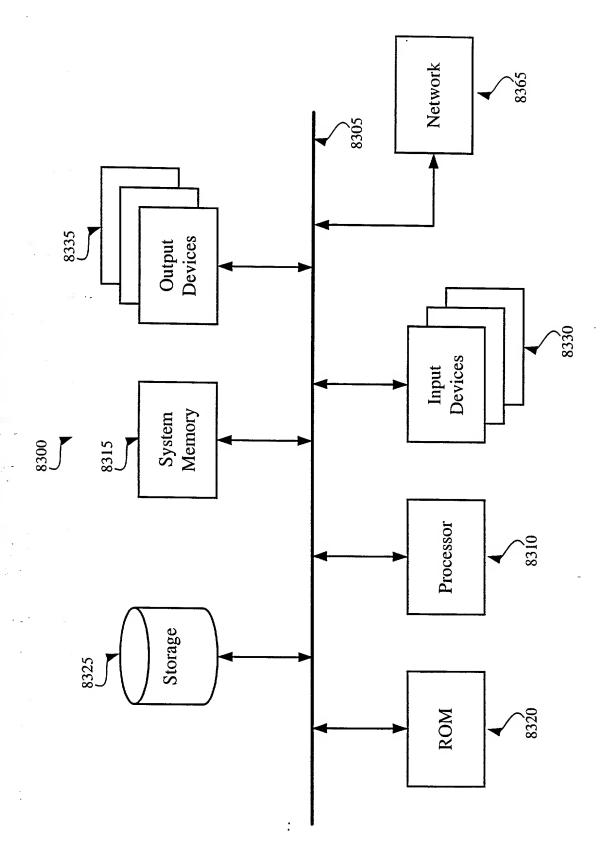


Figure 83